

[54] **RECEIVER CIRCUIT**

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[57] **ABSTRACT**
 The receiver circuit comprises an antenna circuit for supplying an antenna signal having a first frequency F_0 , a local oscillator for supplying an oscillator signal having a second frequency F_{lo} , a mixer circuit for supplying a mixed signal in response to the antenna signal and to the oscillator signal, a circuit for supplying a clock signal having a third frequency F_{ck} equal to F_{lo}/N in which N is a first constant factor, and a sampled data filter for filtering the mixed signal and having a characteristic frequency F_c equal to F_{ck}/K in which K is a second constant factor. In order to simplify the construction of this receiver the local oscillator is arranged in such a way that $F_{lo} = K \cdot N / (K \cdot N \pm 1)$. The receiver circuit is useful in connection with paging receivers.

2 Claims, 1 Drawing Sheet

